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FIG. 11 (PRIOR ART)

DIFFERENCE	SD	MMC	INFLUENCE
INITIALIZATION COMMAND	CMD55+CMD41	CMD1	
CSD VALUE (bit [46:37])	GROUP ERASE PARAMETER DEFINITION	ERASE SECTOR/GROUP PARAMETER DEFINITION	HOST ERASE DEFINITION DIFFERS
COMMAND SUPPORT	ACMDxx FOR SD (CMD55+CMDxx)	SCMDyy FOR S-MMC (CMD55+CMDyy)	COMMAND DIFFERS
OCR VALUE (bit [7:0])	DEFINE VCC = 1.6 TO 2.0V AT bit [7:4]	DEFINE VCC = 1.65 TO 1.95V AT bit [7]	SUPPORT VOLTAGE DEFINITION DIFFERS
OTHER REGISTER	SCR REGISTER (512B) PROVIDED	EX-CSD REGISTER (5128) PROVIDED	SUPPORT FUNCTION AND STATUS CHECK METHOD DIFFER



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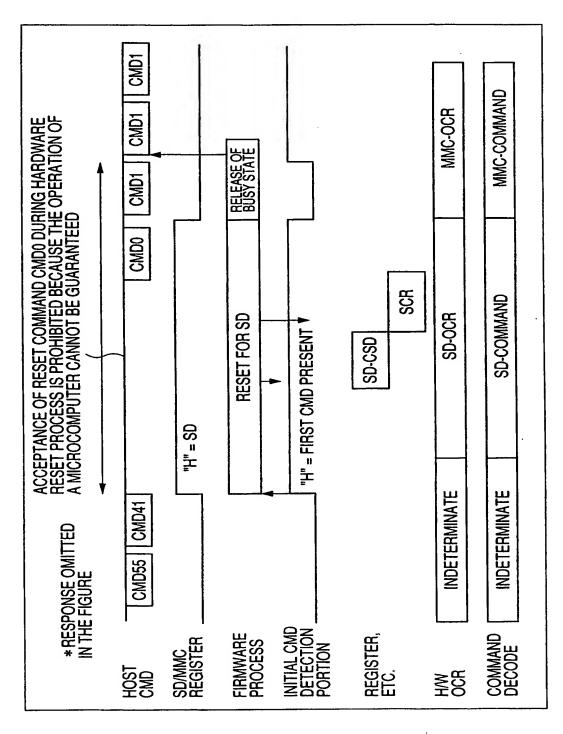
FIG. 12

(BACKGROUND ART) 50: MEMORY CARD READ DATA TO BE SELECTED ACCORDING TO SD/MMC REGISTER CONTROLLER 51 **MICROCOMPUTER** 56 -FLASH **MEMORY** INTERNAL INTERFACE LOGIC SD-CSD MMC-CSD SCR **EX-CSD** SD/MMC REGISTER 62 53 HOST INTERFACE SCR OR EX-CSD 58 -60 59 MMC COMMAND DECODE SD COMMAND DECODE **CSD** MMC-OCR SD-OCR - 52 57--61 55 -54



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FIG. 13 (BACKGROUND ART)





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FIG. 14 (BACKGROUND ART) **POWER-ON** S501 INITIALIZATION CMD ACCEPTED ? NO S504 YES **READ SD-CSD OR MMC-CSD** <S502 SET SD/MMC REGISTER S505 READ SCR OR EX-CSD S503 SWITCH BETWEEN SD COMMAND DECODE AND MMC COMMAND DECODE AND BETWEEN SD-OCR AND MMC-OCR S506 SEARCH FLASH TABLE AND THE LIKE S507 **RELEASE OF BUSY STATE** H/W F/W **COMMAND WAIT**